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 Digital electronics

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|--|----------|------|
| A novel approach of multiplier design based on BCD decoder S Alkurwy Indonesian Journal of Electrical Engineering and Computer Science 14 (1), 38 ... | 8 | 2019 |
| Hardware Implementation of 32-Bit High-Speed Direct Digital Frequency Synthesizer Salah Hasan Ibrahim, Sawal Hamid Md Ali, Md. Shabiul Islam The Scientific World Journal 2014 | 8 | 2014 |
| Implementation of Low Power Compressed ROM for Direct Digital Frequency Synthesizer Salah Hasan Alkurwy, Sawal Hamid Md Ali, Md.Shabiul Islam IEEE-ICSE2014 Proc. 2014, Kuala Lumpur, Malaysia, 309-312 | 7 * | 2014 |
| A low power memoryless ROM design architecture for a direct digital frequency synthesizer S Alkurwy, S Ali, MDS ISLAM, MDF IDROS Turkish Journal of Electrical Engineering & Computer Sciences 25 (5), 4023-4032 | 6 | 2017 |
| High speed direct digital frequency synthesizer with pipelining phase accumulator based on Brent-Kung adder Salah Hasan Ibrahim, Sawal Hamid Md Ali, Md Shabiul Islam Semiconductor Electronics (ICSE), 2012 10th IEEE International Conference on ... | 6 * | 2012 |
| Design a 24-bits pipeline phase accumulator for direct digital frequency synthesizer Salah Hasan Ibrahim, Sawal HM Ali, Md Shabiul Islam Instrumentation & Measurement, Sensor Network and Automation (IMSNA), 2012 ... | 6 * | 2012 |
| Implementation of a 32bit High Speed Phase Accumulator for Direct Digital Frequency Synthesizer MSI Salah Hasan Alkurwy, Sawal Md Ali Asian Journal of Scientific Research 7 (1), 118-124 | 3 | 2014 |
| FPGA Implementation of FIR Filter Design Based on Novel Vedic Multiplier NAD Salah Alkurwy, Saad Al-Azawi International Review on Modelling and Simulations 12 (2), 66 - 71 | 2 * | 2019 |
| 12-bit high speed direct digital frequency synthesizer based on pipelining phase accumulator design Salah Hasan Ibrahim, Sawal Hamid Md Ali, Md Shabiul Islam Journal of Asian Scientific Research 2 (11), 667-672 | 2 | 2012 |
| An area efficient memory-less ROM design architecture for direct digital frequency synthesizer S Alkurwy, SH Ali, MS Islam, F Idros International Journal of Electrical and Computer Engineering 11 (1), 257 | 1 | 2021 |
| Design of a high-performance multiplier based on multiplexer S Alkurwy | 1 | 2018 |

| TITLE | CITED BY | YEAR |
|--|----------|------|
| International Journal of Engineering & Technology 7 (4), 4182-4185 | | |
| A Novel ROM Design for High Speed Direct Digital Frequency Synthesizer MSI Salah Hassan Alkurwy, Sawal Hamid Md Ali LAP LAMBERT PUBLISHING | | 2014 |
| High Speed Direct Digital Frequency Synthesizer Using a New Phase accumulator. Salah Hasan Ibrahim, Sawal Hamid Md Ali, Md Shabiul Islam Journal of Applied Sciences Research 7 (11) | | 2011 |
| Design and Implementation of Parallel Multiplier Using Two Split Circuits S Alkurwy | | |