Republic of Iraq

The Ministry of Higher Education

& Scientific Research

University: **Diyala**
College: **Engineering**
Department: Computer and S/W
Stage: **First**
Lecturer name: **Saad M. Saleh**
Academic Status: **Lecturer**
Qualification: **PhD**
Place of work: Computer Dept.

Flow up the implementation of course syllabus

Course Instructor	Saad Mohammed Saleh
E_mail	saad_alazawi@yahoo.com
Title	Electronic Engineering
Course Coordinator	
Course Objective	To teach student the main information about: Semiconductors, PN-Junction, Diodes, Diode applications, BJT and FET.
Course Description	It introduces the essential information about Electronic Engineering. This includes; Semiconductors, PN-Junction, Diode and its application, BJT and its biasing circuits and equivalent circuits and the FET.
Textbook	Electronic Devices and Circuit Theory by Bolyested Other resources: Electrical Technology by Theraja Other lectures notes on the Internet Network

Course Assessment	First Term	Mid-Year	٢nd Term	Project	Final Exam
	١٠ %	٢٠ %	١٠ %	----	٦٠ %

General Notes	Type here general notes regarding the course

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Course Weekly Outline

week	Date	Topics Covered	Lab. Experiment Assignments	Notes
١	١٩/١١/٢٠١٤	**Introduction**		
٢	٢٦/١١/٢٠١٤	**Insulator, Conductors, Semiconductors**		
٣	٠٣/١٢/٢٠١٤	**P.N Junction**		
٤	١٠/١٢/٢٠١٤	**Diode types**		
٥	١٧/١٢/٢٠١٤	**Half wave rectifier**		
٦	٢٤/١٢/٢٠١٤	**Full wave rectifier**		
٧	٣١/١٢/٢٠١٤	**Clipping circuits**		
٨	٠٧/٠١/٢٠١٥	**Clamping circuits**		
		Mid-Year Break		
٩	١٨/٠٢/٢٠١٥	**Bipolar Junction Transistors**		
١٠	٢٥/٠٢/٢٠١٥	**Basic transistor operation**		
١١	٠٤/٠٣/٢٠١٥	**BJT configurations**		
١٢	١١/٠٣/٢٠١٥	**BJTS basing circuits**		
١٣	١٨/٠٣/٢٠١٥	**BJTS basing circuits**		
١٤	٢٥/٠٣/٢٠١٥	**BJT transistor modeling**		
١٥	٠١/٠٤/٢٠١٥	**RE transistor model**		
١٦	٠٨/٠٤/٢٠١٥	**Hybrid Equivalent model**		
١٧	١٥/٠٤/٢٠١٥	**FET Construction**		
١٨	٢٢/٠٤/٢٠١٥	**FET Operation**		
١٩	٢٩/٠٤/٢٠١٥	**FET Biasing**		
٢٠	٠٦/٠٥/٢٠١٥	**FET Biasing**		
٢١	١٣/٠٥/٢٠١٥	**FET Biasing**		
٢٢	٢٠/٠٥/٢٠١٥	**FET Small-Signal Analysis**		

٢٣	٢٧/٠٥/٢٠١٥	**FET Small-Signal Analysis**		
٢٤	٠٣/٠٦/٢٠١٥	**FET Small-Signal Analysis**		

Instructor Signature: **Dean Signature:**