

Curriculum Vitae

Personal Information:

Name : Hayder Khaleel Ibrahim AL-Qaysi.
General specialization: Electrical Engineering.
Specific specialization: Electronic Engineering.
Occupation : Lecturer.
Birth Date & Place : April 7, 1982, Diyala.
Nationality : Iraqi.
Gender : Male.
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Educational Qualifications:

- M.Sc. degree in electronic engineering, yildiz technical university, faculty of electrical and electronics engineering, Istanbul, Turkey, 2016-2017.
- Master thesis: "Investigation of MOSFET Aging Modeling on an Analog Circuit Design".
- Bachelor's degree in electronic engineering, college of engineering, university of diyala, Iraq, 2003-2004.
 - Grade: Good / Third in sequence. (My rank was fifth to the college)

Courses and Memberships:

- Artificial Neural System Design, Advanced Digital Signal Processing, Computational Intelligence, Medical Electronics System Design and Measurement, Semiconductors Technology, Statistical Signal Processing, Current Mode Circuits, Analog electronics, Digital Electronics, Nano/Micro Electronics.
- Iraqi Engineers Union Membership, 2005-to present.

Languages:

- Arabic, English, Kurdish and Turkish.

Software and Tools:

- MATLAB.
- PSPICE.

- HSPICE.
- OrCAD.
- Windows – Word – Power point – Photoshop and Excel.

Training Courses and Workshops

- A course to pass the English language test (TOFEL) - yildiz technical university, faculty of electrical and electronics engineering, Istanbul, Turkey, 2014.
- Certificate for passing the (GRE) test - yildiz technical university, faculty of electrical and electronics engineering, Istanbul, Turkey, 2014.
- Teaching Methods and Language Safety Course - University of Diyala - 2017.
- Computer Programming Course - University of Diyala - 2017.
- Preparation and participation in more than 50 workshops, training and development courses and seminars in my general and specialized field, 2017-2023.

Work Experiences:

- Lecturer of digital electronics and analog electronics at department of physics, college of sciences, from 2018-till 2020.
- I supervised (3) graduation researches for undergraduate students, department of physics, college of sciences, 2018-2019.
- I supervised (3) graduation researches for undergraduate students, department of physics, college of sciences, 2019-2020.
- Lecturer of digital electronics and analog electronics at department of electronic engineering, college of engineering, from 2020-till now.
- I supervised (2) graduation researches for undergraduate students, department of electronic engineering, college of engineering, 2020-2021.

Other Information:

- Associate director of the central printing press department, university of diyala, 2010-2014, 2019-2020.
- Director of laboratory reliability unit, college of sciences, university of diyala, 2017-2019.
- Director of the central printing press department, university of diyala, 2019-2020.
- Providing a special study on the work plan of a good laboratory practices (GLP) requirements approved by the council of college of sciences, university of diyala, in the interest of the work of the university, 2018.
- Director of the student affairs and registration section, college of engineering, university of diyala, 2021 until now.
- Thanks and appreciation from the president of the university.
- Thanks and appreciation from the deans of colleges.
- Financial rewards from the university president and deans of colleges.

Publications Journal:

ت	اسم البحث	اسم المجلة	دار النشر	ISSN
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1	Enhancing the Gain and Power of Folded-cascode Amplifier using Artificial Neural Network	International Journal of Engineering Research and Technology	International Research Publication House	0974-3154
2	COMPREHENSIVE STUDY ON UNMANNED AERIAL VEHICLES (UAVs)	Advanced Mathematical Models & Applications	Jomard Publishing	2519-4445
3	Design of very low-voltages and high-performance CMOS gate-driven operational amplifier	Indonesian Journal of Electrical Engineering and Computer Science	Institute of Advanced Engineering and Science (IAES)	2502-4752
4	Evaluation of electrical load estimation in Diyala governorate (Baaquba city) based on fuzzy inference system	International Journal of Electrical and Computer Engineering (IJECE)	Institute of Advanced Engineering and Science (IAES)	2088-8708
5	Evaluation of different quantization resolution levels on the BER performance of massive MIMO systems under different operating scenarios	Indonesian Journal of Electrical Engineering and Computer Science	Institute of Advanced Engineering and Science (IAES)	2502-4752
6	Reliability-based routing metric for UAVs networks	Indonesian Journal of Electrical Engineering and Computer Science	Institute of Advanced Engineering and Science (IAES)	2502-4752
7	An Improvement the Channel Characteristics Performance of Ultra-Wideband (UWB) by Controlling the Main Channel Parameters	Design Engineering (Toronto)	Rogers Media Publishing Ltd	0011-9342
8	Design methodology for general enhancement of a single-stage self-compensated folded-cascode operational transconductance amplifiers in 65 nm CMOS process	International Journal of Electrical and Computer Engineering (IJECE)	Institute of Advanced Engineering and Science (IAES)	2088-8708
9	Design of CRC circuit for 5G system using VHDL	Bulletin of Electrical Engineering and Informatics	Institute of Advanced Engineering and Science (IAES)	2302-9285
10	Design LC oscillator for MF, HF& VHF using both ideal and practical operation amplifier	Periodicals of Engineering and Natural Sciences	International University of Sarajevo	2303-4521

Publications Conference:

No	اسم المؤتمر	عنوان البحث	جهة النشر
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1	Proceedings of International Conference on Progress in Applied Science 2017 (ICPAS 2017), 04-06 January 2017, Istanbul, Turkey. ISBN: 978-605-9546-02-7.	Investigation of MOSFET Aging Modeling on an Analog Circuit Design	JOURNAL OF THERMAL ENGINEERING
2	2021 International Conference on Intelligent Technologies (CONIT), 25-27 June 2021, Hubli, India.	The Effect of Antenna Height on the Performance of the Okumura/Hata Model Under Different Environments Propagation	IEEE
3	2022 2nd International Conference on Intelligent Technologies (CONIT)	Design & Simulation of a High Frequency Rectifier Using Operational Amplifier	IEEE
4	2023 3rd International Conference on Intelligent Technologies (CONIT)	An Algorithmic State Machine Design Approach for Digital Divider Controller	IEEE