Curriculum Vitae _Yasir Amer Abbas

Objective

To participate and have a position of responsibility in the fields of Computer Engineering (fundamental of Logic design, Computer Architectures, Embedded system design, Hardware cryptography using FPGA) that enables me to utilize my skills and theoretical expertise to serve university teaching and working.

Personal Information

Academic degree: Assistant Professor

ORCID: 0000-0002-4017-4481

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Birth Date & Place: 1975 – Iraq

Nationality: Iraqi

Married – with two children

Mobile: Iraq\+9647736458962

Email: yasiramerabbas@gmail.com and dr.yasiral-zubaidi@uodiyala.edu.iq

Website: www.engineering.uodiyala.edu.iq

License: Iraqi International Driving License.

Postal address: Department of Computer Engineering, College of Engineering, University of

Diyala, Baqubah City, Diyala Governorate, ZIP 964, Iraq.

Professional Strength and Skills

- Very effective in fundamental of Logic design, Computer Architectures, embedded system design, Hardware cryptography using FPGA).
- Wide theoretical knowledge in most subject of computer engineering such as electrical circuits, electronics, mathematics, and embedded system design.

Educational Qualifications

- **Ph.D. in** Computer Engineering 2016 University Tenaga Nasional Malaysia (English Curriculum)
- Ms. C. Computer Engineering 2005, University of Technology Iraq. (English Curriculum).
- **B.Sc.** Electrical and Electronic Engineering 2000 Al-Rashied College of engineering & science University of Technology Iraq. (English Curriculum).

Memberships and Academic positions

- Lecturer of Computer and Software Engineering Department -Al-Rashied college of engineering
 & science University of Technology Iraq from 2000 to 2007
- Director of continuing education unit in Engineering College, University of Diyala since 2007 –
 2008.

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 Rapporteur of Computer and Software Engineering Department - College of Engineering – University of Diyala since 2008 – 2011.

- Lecturer of Computer and Software Engineering Department College of Engineering University of Diyala since 2007 to 2011
- Member of Examination committee and scientific research committee in department of computer and software engineering, Engineering College, University of Diyala since 2007 – 2011.
- Member of Scientific Committee of Computer Engineering Department College of Engineering
 University of Diyala 2016 till now.
- Laboratory Manager of Computer Engineering Department College of Engineering University of Diyala 2016 to 2018
- Member of Iraqi Engineers Union (IEU) since 2000 till now.
- Member of IEEE since 2012 until no
- Member of IEEE Computer Society 2012 until now.
- Senior IEEE Member since 2016 until now.
- Responsible of E-learning in University of Diyala since 2020 until now

Training and Courses

- Project Cycle management and proposal writing in The Queen Zain Al Sharaf Institute for Development, Amman, Jordan, 15-17 August 2005
- Protection in the Context of Humanitarian Assistance in Iraq, DRC, Amman, Jordan, 2005
- Methods of Teaching Course for one month in University of Technology Iraq 2005.
- Test the safety of the Arabic language in University of Diyala 2007.
- Teaching validity test in University of Diyala 2007.
- IC3 in Computer and Internet Center University of Diyala— Iraq 2011.
- Innovation and Entrepreneur Seminar, Malaysia, 28-29 April 2012
- IEEE Malaysia student congress, Malaysia, 2013
- IEEE Malaysia student congress, Malaysia, 2014
- IEEE Malaysia Young Professional (YP) & Student Workshop, Malaysia, 2016
- Competency-Based Learning: Introduction, WHO, Iraq, March 2020
- Tips for Preparing Virtual e-learning lessons, E-learning in Iraqi Project Ministerial Team, March, 2020
- Free Video Conference Platform for Interactive E-learning in Iraqi Project Ministerial Team, March 2020
- Tips for users of Google Educational Applications in the Iraqi Education Environment, E-learning in Iraqi
 Project Ministerial Team, April 2020

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Languages

• English: Written and spoken (Very Good)

• Arabic: Mother tongue.

Employment History (Academic & Technical)

University of Diyala – Computer Center

(2020 – until now)

Location: Diyala – Iraq

Position: Manager of Computer Center Academic degree: Assistant professor.

University of Diyala - College of Engineering

(2007 - 2020)

Location: Diyala – Iraq

Academic degree: Assistant professor.

Description: Teaching Embedded System, Digital system Design for the Fourth and Third stages respectively in Computer Engineering Department.

University of Technology – Al-Rashied college of engineering & science

(2000 - 2007)

Location: Baghdad – Iraq

Position: lecturer

Description: Teaching Computer Architectures and Fundamental of Logic Design for the first and second stages respectively in Computer Engineering Department.

Supervision of Theses and dissertations

- Supervising the Master student in the Department of Computer Engineering, College of Engineering-Iraqi University, Iraq.
- Supervising the graduation projects of the fourth stage in the Department of Computer Engineering,
 College of Engineering- University of Diyala, Iraq.

Reviewer and referee

Reviewer and referee in:

- 1. Diyala Journal of Engineering Sciences College of Engineering University of Diyala.
- 2. IEEE Transactions on Circuits and Systems for Video Technology.
- 3. IET Circuits, Devices and Systems.
- 4. Iraqi Journal o, f Computer, Communication, Control & System Engineering University of Technology.
- 5. IET Science, Measurement and Technology.
- 6. IET Communications.
- 7. KSII Transactions on Internet and Information Systems.
- 8. IET Networks.

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Awards and Prizes

- The best paper award IEEE Conference (PECON 2014)- Malaysia -2014
- The Silver medal in Korea, Seoul International Invention Fair 2014 (SIIF14), Korea 2014.

Published Research Papers

No	Title of research	Journal name	Publisher or hosting	ISSN
1	Low Power and High Speed Sequential Circuits Test Architecture	Recent Patents on Computer Science	Bentham Science Publishers	
2	A SYSTEMATIC MAPPING STUDY ON RADIO FREQUENCY IDENTIFICATION SECURITY	International Journal of Sensors, Wireless Communication and Control	Bentham Science Publishers	2210-3279
3	Test Mode for FIR Filter Design Using FPGA.	Journal of Advanced Research in Dynamical and Control Systems	Institute of Advanced Scientific Research	6473-6480
4	Small Footprint Mix-Column Serial for PHOTON and LED Lightweight Cryptography	ICOASE 2018	IEEE	
5	Robust skin diseases detection and classification using deep neural networks	International Journal of Engineering and Technology	Science Publishing Corporation Inc	227524X
6	Photon: A new mix columns architecture on FPGA.	International Journal of Engineering and Technology	Science Publishing Corporation Inc	227524X
7	The effect of uniaxial strain on the electrical properties of graphene nanoribbon	ISCES 2018	IEEE	

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<u>Yasir Amer Abbas</u> CV

8	FPGA Implementation of DLP-PHOTON Hash Function	International Journal of Future Generation Communication and Networking	Science and Engineering Research Support Society	22079645
9	PRINCE IP-Core on Field Programmable Gate Arrays (FPGA).	Res. J. Appl. Sci. Eng. Technol	Maxwell Scientific Organization	2040-7459
10	Lightweight PRINCE Algorithm IP Core for Securing GSM Messaging using FPGA.	Research Journal of Information	Academic Journals Inc.	1815-7432
11	Reusable Data-Path Architecture for Encryption- then-Authentication on FPGA.	International Review on Computers and Software (IRECOS)	Praise Worth Prize	1828-6003
12	Efficient FPGA Architectures for Dual Mode Integer Haar Lifting Wavelet Transform Core.	Journal of Applied Sciences	Asian Network for Scientific Information	1812-5654
13	Securing electrical substation's wireless messaging with a Lightweight Crypto-Algorithm IP core	PECON 2014	IEEE Explor Digital libaray	4799-7297- 5/14
14	Implementation of PRINCE Algorithm in FPGA	ICIMU 2014	IEEE Explor Digital libaray	4799-5423- 0/14
15	Low complexity multidimensional CDF 5/3 DWT architecture	CSNDSP 2014	IEEE	4799-2581- 0/14
16	Evaluation of Reconfigurable Technique for Heterogeneous Multi-core Processors with FPGA Platform	SASSP 2013	IEEE	
17	Reconfigurable Design of Heterogonous Multiprocessors using FPGA Platforms: A Review	NatGrad2012	UNITEN	

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Conferences

No	Conference	Type of activity	Notes
1	ICIMU 2020	Publish Paper	
2	ICICT 2019	Participant	
3	ICOASE 2018	Publish Paper	
4	ISCES 2018	Publish Paper	
5	ASCFYP2019	Committees	
6	ASCFYP2019	Committees	
7	PECON 2014	Publish Paper	
8	ICIMU 2014	Publish Paper	
9	CSNDSP 2014	Publish Paper	
10	SASSP 2013	Publish Paper	
11	NatGrad2012	Publish Paper	
12	ICICT2019	Participant	
13	2013 IEEE Malaysia Student Congress	Participant	
14	2014 IEEE Malaysia Student Congress	Participant	

For more information visit the following links of Social and Scientific media

Research Gate

https://www.researchgate.net/profile/Yasir_Abbas4/info

Acadimeca.edu

https://uodiyala.academia.edu/YasirAmer

https://scholar.google.com/citations?user=zIrTVRMAAAAJ&hl=en

https://publons.com/author/1218858/yasir-amer-abbas#profile

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